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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10090483	FILING DATE 03/01/2002	CLASS 281.345	SUBCLASS 87	GAU 3673 2811	EXAMINER Piziel;
**APPLICANTS: Kobayashi Kazuo;					
**CONTINUING DATA VERIFIED: None - 00					
<h2>BEST AVAILABLE COPY</h2>					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-059045(P) 03/02/2001 00					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials <i>00</i>				81754.0072	
TITLE : Semiconductor integrated circuit for successively scanning lines of electrodes of an image display apparatus					
<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
PREPARED FOR ISSUE			
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